**Class Schedule - Spring 2019**

**Electrical and Computer Engineering**

**ECE 483  Analog IC Design**  credit: 3 hours.

Basic linear integrated circuit design techniques using bi-polar, JFET, and MOS technologies; operational amplifiers; wide-band feedback amplifiers; sinusoidal and relaxation oscillators; electric circuit noise; application of linear integrated circuits. 3 undergraduate hours. 3 graduate hours. Prerequisite: ECE 342.

<table>
<thead>
<tr>
<th>CRN</th>
<th>Type</th>
<th>Section</th>
<th>Time</th>
<th>Days</th>
<th>Location</th>
<th>Instructor</th>
</tr>
</thead>
<tbody>
<tr>
<td>33973</td>
<td>Discussion/Recitation</td>
<td>L</td>
<td>10:00 AM - 11:20 AM</td>
<td>MW</td>
<td>2017 - Electrical &amp; Computer Eng Bldg</td>
<td>Hanumolu, P</td>
</tr>
</tbody>
</table>