Class Schedule - Fall 2019

Electrical and Computer Engineering

ECE 542  Fault-Tolerant Dig Syst Design  credit: 4 hours.
Advanced concepts in hardware and software fault tolerance: fault models, coding in computer systems, module and system level fault
detection mechanism, reconfiguration techniques in multiprocessor systems and VLSI processor arrays, and software fault tolerance
techniques such as recovery blocks, N-version programming, checkpointing, and recovery; survey of practical fault-tolerant systems.
Same as CS 536. Prerequisite: ECE 411.

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<th>CRN</th>
<th>Type</th>
<th>Section</th>
<th>Time</th>
<th>Days</th>
<th>Location</th>
<th>Instructor</th>
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<tbody>
<tr>
<td>49836</td>
<td>Lecture-Discussion</td>
<td>C</td>
<td>09:30 AM - 10:45 AM</td>
<td>TR</td>
<td>3013 - Electrical &amp; Computer Eng Bldg</td>
<td>Kalbarczyk, Z</td>
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</tbody>
</table>

Credit Hours: 4 hours
Restricted to Graduate - Urbana-Champaign.