Computer Science

CS 431  **Embedded Sys Arch and Software**  credit: 0 TO 4 hours.
Survey of sampled data systems and embedded architecture; overview of the key concepts in common embedded system applications; signal processing and control; embedded microprocessor and device interface; time-critical I/O handling; data communications; real-time operating systems and techniques for the development and analysis of embedded real-time software; hands-on laboratory projects. 3 undergraduate hours only. 3 or 4 graduate hours only. Prerequisite: CS 241 or CS 423.

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<thead>
<tr>
<th>CRN</th>
<th>Type</th>
<th>Section</th>
<th>Time</th>
<th>Days</th>
<th>Location</th>
<th>Instructor</th>
</tr>
</thead>
<tbody>
<tr>
<td>40100</td>
<td>Laboratory</td>
<td>AB1</td>
<td>10:00 AM - 11:50 AM</td>
<td>T</td>
<td>ARR - Siebel Center for Comp Sci</td>
<td>Caccamo, M</td>
</tr>
<tr>
<td>40101</td>
<td>Laboratory</td>
<td>AB2</td>
<td>02:00 PM - 03:50 PM</td>
<td>T</td>
<td>ARR - Siebel Center for Comp Sci</td>
<td>Caccamo, M</td>
</tr>
<tr>
<td>31526</td>
<td>Laboratory</td>
<td>AB3</td>
<td>03:00 PM - 04:50 PM</td>
<td>W</td>
<td>ARR - Siebel Center for Comp Sci</td>
<td>Caccamo, M</td>
</tr>
<tr>
<td>40102</td>
<td>Laboratory</td>
<td>AB4</td>
<td>05:00 PM - 06:50 PM</td>
<td>W</td>
<td>ARR - Siebel Center for Comp Sci</td>
<td>Caccamo, M</td>
</tr>
<tr>
<td>40667</td>
<td>Lecture-Discussion</td>
<td>AE3</td>
<td>11:00 AM - 12:15 PM</td>
<td>WF</td>
<td>1310 - Digital Computer Laboratory</td>
<td>Caccamo, M</td>
</tr>
</tbody>
</table>

Credit Hours: 3 hours

<table>
<thead>
<tr>
<th>CRN</th>
<th>Type</th>
<th>Section</th>
<th>Time</th>
<th>Days</th>
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<th>Instructor</th>
</tr>
</thead>
<tbody>
<tr>
<td>40668</td>
<td>Lecture-Discussion</td>
<td>AE4</td>
<td>11:00 AM - 12:15 PM</td>
<td>WF</td>
<td>1310 - Digital Computer Laboratory</td>
<td>Caccamo, M</td>
</tr>
</tbody>
</table>

Credit Hours: 4 hours
Restricted to Graduate - Urbana-Champaign.
Restricted to graduate students.