

Course Schedule - Fall 2007

Computer Science

231 **Computer Architecture I** credit: 3 hours.

Introduction to computer architecture, working up from the logic gate level: combinational and sequential networks; computer arithmetic; arithmetic/logic units; memory organization; control unit design. Credit is not given for both CS 231 and ECE 290. Prerequisite: CS 125.

This course satisfies the General Education Criteria for a Quant Reasoning II course.

CRN	Type	Section	Time	Days	Location	Instructor
30105	lecture	X	11:00 AM - 11:50 AM	MW	room 1404 Siebel Center for Comp Sci	Kale, L; Smith, J
30105: Quant Reasoning II course.						
30105: A review session will be held each week at 11:00 on Friday, 1404 Siebel Center. Students should adjust their schedule accordingly.						